

COMBINED DECLARATION FOR PATENT APPLICATION  
AND  
POWER OF ATTORNEY

As below-named inventors, we hereby declare that:

Our residences, post office addresses and citizenships are as stated below, next to each name.

We believe we are original joint inventors of the subject matter which is claimed and for which a patent is sought on the invention entitled:

**Superconductors on Iridium Substrates and Buffer Layers**

the specification of which is attached hereto.

We hereby state that we have reviewed and understand the contents of the above-identified specification, including the claims.

We acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

We hereby appoint the following attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

George L. Craig	Registration No. 29,293
Judson R. Hightower	Registration No. 26,603
Joseph A. Marasco	Registration No. 32,798
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All of: UT-Battelle, LLC  
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Please direct all correspondence and telephone calls to Joseph A. Marasco at 865-574-4178.

We hereby declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first inventor: **Mariappan P. Paranthaman**

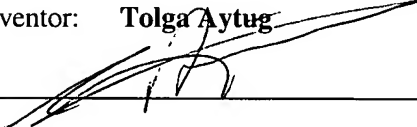
Inventor's signature: Mariappan P. Paranthaman Date: 03/22/2004

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Full name of second inventor: **Tolga Aytug**

Inventor's signature:  Date: 03/22/2004

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